

ARC.015RC1



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant	:	Peter Warnes	
App. No.	:	09/808,469	27299
Filed	:	March 14, 2001) 2/299) PATENT TRADEMARK OFFIC
For	:	METHOD AND APPARATUS FOR PROCESSOR CODE OPTIMIZATION USING CODE COMPRESSION))))
Examiner	:	Steelman, Mary J.)
Group Art U	nit:	2122)))

INFORMATION DISCLOSURE STATEMENT

Mail Stop RCE Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

Enclosed is form PTO-1449 listing twenty (20) references that are also enclosed. This Information Disclosure Statement is being filed before the mailing date of a first Office Action on the merits, and presumably no fee is required in accordance with 37 C.F.R. §1.97(b)(3). If a first Office Action on the merits was mailed before the mailing date of this Statement, it is requested that the Commissioner charge any fees under 37 C.F.R. § 1.16 and § 1.17 to Deposit Account No. 501423.

App. No.

09/808,469

Filed

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4/18/05

March 14, 2001

Pursuant to 37 C.F.R. § 1.97(g) and (h), Applicant makes no representation that a search has been made or that the information cited is considered to be material to patentability. Additionally, inclusion on this list is not an admission that any of the cited documents are prior art in this application. Further, Applicant makes no representation regarding the completeness of this list or that better art does not exist.

Respectfully submitted,

GAZDZINSKI & ASSOCIATES

Dated:

By:

Robert F. Gazdzinski, Esq.

Registration No. 39,990

11440 West Bernardo Court, Suite 375

San Diego, CA 92127

Telephone: (858) 675-1670

Facsimile: (858) 675-1674

		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE DISCLOSURE STATEMENT	ATTY. DOCKET NO. ARC.015RC1	APPLICATION NO. 09/808,469
'	8 The	APPLICANT	APPLICANT Peter Warnes	
	USE SEVERAL	. SHEETS IF NECESSARY)	FILING DATE March 14, 2001	GROUP

U.S. PATENT DOCUMENTS							
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE (IF APPROPRIATE)
	1	5,493,508	2/20/96	Dangelo, et al.			
	2	5,535,331	7/9/96	Swoboda et al.			
	3	5,544,067	8/6/96	Rostoker, et al.			
1	4	5,555,201	9/10/96	Dangelo, et al.			
	5	5,801,958	9/1/98	Dangelo et al.			
	6	5,867,399	02/1999	Rostoker, et al.			
	7	6,173,434	01/2001	Wirthlin, et al.			
	8	6,324,678	11/2001	Dangelo, et al.			
	9	6,378,123	07/2002	Dupenloup			
	10	6,421,818	07/2002	Dupenloup, et al.			
	11	6,477,697 B1	11/5/02	Killian, et al.			
	12	6,760,888	7/6/04	Killian, et al.			
	13	6,862,563	03/01/05	Hakewill, et al.			

FOREIGN PATENT DOCUMENTS								
EXAMINER		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
INITIAL							YES	NO
	14	2 308 470	6/97	GB				
	15	WO 97 13209 A	04/1997	WIPO				

EXAMINER INITIAL	OTHER DOCUMENTS (INCLUDING AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.)						
	16	Elms, A., "Tuning a Customisable RISC Core for DSP," Electronic Product Design, Sept. 1997, Vol. 18, No. 9, pages 19-20, 22, XP000909039					
	17	Tensilica, Inc; Xtensa Instruction Set Architecture (ISA) Reference Manual Revision 1.0. (First 25 pages)					
	18	Tensilica, Inc. Instruction Extension Language (TIE) Reference Manual Revision 1.3.					
	19	Compton et al., "Configurable Computing: A Survey of Systems and Software," Technical Report, Northwestern University, Dept. of ECE, 1999.					
	20	Yang, Jin-Hyuk, et al., "MetaCore: A Configurable & Instruction-Level Extensible DSP Core," Proceedings of the ASP-DAC '98 Asian and South Pacific Design Automation Conference 1998, pages 325-326, XP-002137268					

EXAMINER	DATE CONSIDERED
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*EXAMINER: INITIAL IF CITATION CONSIDERED, WHETHER OR NOT CITATION IS IN CONFORMANCE WITH MPEP 609; DRAW LINE THROUGH CITATION IF NOT IN CONFORMANCE AND NOT CONSIDERED, INCLUDE COPY OF THIS FORM WITH NEXT COMMUNICATION TO APPLICANT.